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S/N 09/866,938

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Wendell P. Noble Jr. et al.

Examiner: Jack Chen

Serial No.: 09/866,938

Group Art Unit: 2813

Filed: May 29, 2001

Docket: 303.330US3

Title: ULTRA HIGH DENSITY FLASH MEMORY

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**RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT**

MS Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

In response to the Notice of Non-Compliant Amendment mailed February 8, 2005, please amend the above identified application as follows.

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**MS Amendment**

Commissioner for Patents

P.O. Box 1450

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We are transmitting herewith the following attached items (as indicated with an "X"):

X A return postcard.

X A Response to Notice of Non-Compliant Amendment (20 Pages).

X A Communication Concerning Related Applications (2 pgs.).

If not provided for in a separate paper filed herewith, Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number 21186

By: Daniel J. Kluth

Atty: Daniel J. Kluth

Reg. No. 32,146

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 8 day of March, 2005.

Tina Kohut  
Name

2/11  
Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

(GENERAL)



S/N 09/866,938

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COMMUNICATION CONCERNING RELATED APPLICATION(S)

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
10/361986 6798009	February 11, 2003	303.380US4	CIRCUIT AND METHOD FOR AN OPEN BIT LINE MEMORY CELL WITH A VERTICAL TRANSISTOR AND TRENCH PLATE TRENCH CAPACITOR
10/738449	December 16, 2003	303.412US4	VERTICAL GAIN CELL AND ARRAY FOR A DYNAMIC RANDOM ACCESS MEMORY AND METHOD FOR FORMING THE SAME

COMMUNICATION CONCERNING RELATED APPLICATIONS

Serial Number: 09/866,938

Filing Date: May 29, 2001

Title: ULTRA HIGH DENSITY FLASH MEMORY

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Dkt: 303.330US3

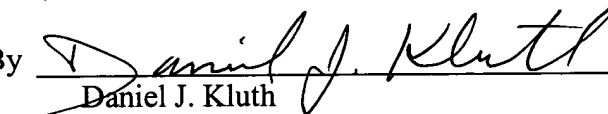
10/879378      June 29,      303.379US3      CIRCUIT AND METHOD FOR A  
2004      FOLDED BIT LINE MEMORY CELL  
WITH VERTICAL TRANSISTOR AND  
TRENCH CAPACITOR

Respectfully submitted,

WENDELL P. NOBLE JR. ET AL.

By Applicants' Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
P.O. Box 2938  
Minneapolis, MN 55402  
(612) 373-6904

Date Mar 8, 2005 By   
Daniel J. Kluth  
Reg. No. 32,146

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 8 day of March, 2005.

Name

Tina Kohak

Signature

